

Notice of References Cited	Application/Control No. 09/883,503	Applicant(s)/Patent Under Reexamination SOUDIER, CHRISTOPHE	
	Examiner Thomas H. Stevens	Art Unit 2123	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-3,723,718	03-1973	Jaffe et al.	703/4
	B	US-5,732,192	03-1998	Malin et al.	703/2
	C	US-			
	D	US-			
	E	US-			
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Nasereddin et al., "The Development of a Methodology for the Use of Neural Networks and Simulation Modeling in System Design". 1999 Winer Simulation Conference pg.537-342
	V	Nolan et al., "Assessment of Ways of Improving the Supply Continuity in Electronic Power System--A Simulation Approach", 1993 Winter Simulation Conference pg.1192-1200.
	W	Romanowicz et al., "Modeling and Simulation of Electromechanical Transducers in Microsystems Using an Analog Hardware Description Language" 1997 IEEE pg. 119-123.
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.